

2814

XA-9590

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re the application of:

Masao SHINOZAKI et al.

Serial No.: 10/002,009

Group Art Unit: 2814

Filed: December 5, 2001

Examiner: S. Rao

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING  
A SEMICONDUCTOR DEVICE

\* \* \*

RESPONSE

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

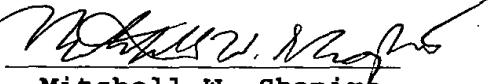
Responding to the Office Action mailed May 16, 2002 and to the requirement for restriction therein, applicants elect to prosecute in this application the invention designated as Group I (Claims 1-7 and 21-26). Applicants reserve the right to file one or more divisional patent applications with regard to the non-elected invention.

An action on the merits of the claims to the elected invention is respectfully requested.

RECEIVED  
JUN 10 2002  
TECHNOLOGY CENTER 2800

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§1.16 and 1.17 which may be required by this paper, and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, then such extension is hereby requested.

Respectfully submitted,

By:   
Mitchell W. Shapiro  
Reg. No. 31,568

MWS:lat

Miles & Stockbridge P.C.  
1751 Pinnacle Drive  
Suite 500  
McLean, Virginia 22102  
(703) 903-9000

June 7, 2002